

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/008,958	12/05/2001	Miki Sasaki		5427	
26021	7590 05/25/2004		EXAMINER		
HOGAN & HARTSON L.L.P.			HA, NA	HA, NATHAN W	
500 S. GRAI SUITE 1900	ND AVENUE	•	ART UNIT	PAPER NUMBER	
	LES, CA 90071-2611		2814	٠.	
			DATE MAILED: 05/25/200	4	

Please find below and/or attached an Office communication concerning this application or proceeding.

· · · · · · · · · · · · · · · · · · ·	•						
	Application No.	Applicant(s)					
	10/008,958	SASAKI ET AL.					
Office Action Summary	Examiner	Art Unit	 				
	Nathan W. Ha	2814					
The MAILING DATE of this communication ap	pears on the cover sheet with	h the corresp ndence add	lress /				
	VIO OFT TO EVENE AMO	NITU(C) EDOM					
A SHORTENED STATUTORY PERIOD FOR REPI THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37,CFR 1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a re - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statu Any reply received by the Office later than three months after the maili earned patent term adjustment. See 37 CFR 1.704(b).	.136(a). In no event, however, may a rep ply within the statutory minimum of thirty d will apply and will expire SIX (6) MONT te. cause the application to become ABA	oly be timely filed (30) days will be considered timely. HS from the mailing date of this con INDONED (35 U.S.C. § 133).	nmunication.				
Status							
1) Responsive to communication(s) filed on 11	March 2004						
• • • • • • • • • • • • • • • • • • • •	is action is non-final.						
		rs, prosecution as to the	merits is				
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
closed in accordance with the practice under	Ex parte Quayle, 1999 O.D.	11, 400 0.0. 210.					
Disposition of Claims		•					
4) Claim(s) 1,2 and 4-13 is/are pending in the a	pplication.		•				
4a) Of the above claim(s) is/are withdr	awn from consideration.						
5) Claim(s) is/are allowed.							
6)⊠ Claim(s) <u>1,2 and 4-13</u> is/are rejected.							
7) Claim(s) is/are objected to.							
8) Claim(s) are subject to restriction and	or election requirement.						
<u>, </u>							
Application Papers	en de la composition de la composition La composition de la						
9)☐ The specification is objected to by the Examir							
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) ☐ The oath or declaration is objected to by the I	Examiner. Note the attached	Office Action or form PT	O-152.				
Priority under 35 U.S.C. § 119			*				
12) Acknowledgment is made of a claim for foreign	gn priority under 35 U.S.C. §	119(a)-(d) or (f).					
a) ☐ All b) ☐ Some * c) ☐ None of: 1. ☐ Certified copies of the priority docume	nte have been received	;					
		onlication No					
2. Certified copies of the priority docume			Stago				
3. Copies of the certified copies of the pr		received in this ivational	Stage				
application from the International Bure							
* See the attached detailed Office action for a li	st of the certified copies not i	receivea.					
			en e				
	•		$S_{k} = \{ \{ 1, \dots, k \} \} $				
Attachment(s)	·						
1) Notice of References Cited (PTO-892)		ummary (PTO-413)	:				
2) Notice of Draftsperson's Patent Drawing Review (PTO-948))/Mail Date formal Patent Application (PTC)-152)				
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/C Paper No(s)/Mail Date	6) Other:	en e					

Application/Control Number: 10/008,958

Art Unit: 2814

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(f) he did not himself invent the subject matter sought to be patented.

2. Claims 1-2 and 4-13 are rejected under 35 U.S.C. 102(f) because the applicant did not invent the claimed subject matter. For example, the Applicant's Admitted prior Art, AAPA, includes all claimed limitations as described below.

In regard to claims 1 and 11, in fig.1, the AAPA discloses a semiconductor device comprising:

a dicing region 103 provided on a semiconductor substrate 101 to separate a plurality of semiconductor chips each having a gate portion from each other;

a plurality of element isolation regions, included in region 104, provided on a surface portion on the semiconductor substrate within the dicing region; a plurality of first dummy patterns 114-116 formed on a surface of the semiconductor substrate so as to correspond to intervals of the plurality of element isolation regions, respectively; and

a plurality of second dummy patterns 123 and 127 formed above the semiconductor substrate within the dicing region so as to correspond to the plurality of first dummy patterns, respectively.

Application/Control Number: 10/008,958

Art Unit: 2814

In regard to claims 2 and 13, wherein the dummy pattern has a wiring structure which is substantially equal to that of the gate portion. See fig. 1.

In regard to claim 4 wherein the plurality of first dummy patterns each have a structure which is substantially similar to that of the first gate portion. See fig. 1.

In regard to claim 5, wherein the plurality of first dummy patterns and the gate portions each have a laminated structure including a gate oxide film, a polysilicon film, a WSi film, and a SiN film. See the discussion of the AAPA, page 9.

In regard to claim 6, wherein the plurality of element isolation regions each have an STI structure 112, for example.

In regard to claim 7, figure 1 shows only a portion of the whole wafer. Therefore, the whole wafer should disclose this structure repeatedly.

In regard to claims 8-9, wherein the plurality of second dummy patterns include at least protection films provided on the surface of the semiconductor substrate, not numbered. See fig. 1.

In regard to claim 10, wherein the dummy pattern is formed along a dicing direction, in region 137.

In regard to claim 12, wherein the dummy pattern is formed concurrently with formation of the gate portion. See fig.1.

Response to Arguments

3. Applicant's arguments with respect to claims 1-2 and 4-13 have been considered but are moot in view of the new ground(s) of rejection.

Application/Control Number: 10/008,958

Art Unit: 2814

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nathan W. Ha whose telephone number is (571) 272-1707. The examiner can normally be reached on M-TH 8:00-7:00(EST).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Nathan Ha May 18, 2004

LONG PHAM
PRIMARY EXAMINER